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COMP_ENG 361: Computer Architecture I

Cache And Memory Hierarchy Design

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Scaling Simulation

Assignment 3

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memory hierarchy design and its impact on overall processor performance. Design cache memory based on the characteristics of the expected workload. Understand the workings of virtual memory ...COMP_ENG 361: Computer Architecture I Large memories (DRAM) are slow Small memories (SRAM) are fast Make the average access time small by: Servicing most accesses from a small, fast memory. Reduce the bandwidth required of the large ...Cache Memory Has simulation performance stagnated, and what is the industry doing to correct it? Without functional simulation the semiconductor industry would not be where it is today, but some people in the ...Scaling Simulation Chapter 2 discussed how multiple levels of cache work together to

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