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Financial Highlights 66 6.1 Financial Status and Operating Results 66 6.2 Risk Management 71 7. Corporate Social Responsibility 78 7.1 Environmental, Safety and Health (ESH) Management 80 7.2 TSMC Education and ...TSMC VISION & CORE VALUESA strong portfolio of intellectual property rights strengthens TSMC's technology leadership and protects our advanced and leading edge technologies. In 2014, TSMC received a record breaking 1460 U.S. patents, as well as 450+ issued patents in Taiwan and the PRC, and other patents issued in various other countries.Technology Leadership | Operational Highlights - TSMC5.2 Technology Leadership 5.2.1 R&D Organization and Investment In 2008, TSMC increased its research and development investments and strengthened its R&D organization both in advanced and mainstream technologies. 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TSMC will retain the process leadership it has since its 10nm node in ...Report: TSMC 5nm Chip Process Already Fully Booked | Tom's ...AMD partners with TSMC to lead the industry with groundbreaking 7nm technology. \*\*\* Subscribe: [http://bit.ly/Subscribe\\_to\\_AMD](http://bit.ly/Subscribe_to_AMD) Like us on Facebook: [http://bit.ly/Subscribe\\_to\\_AMD](http://bit.ly/Subscribe_to_AMD) AMD and TSMC Partner on Industry Leading 7nm Process NodeAs the industry heads down the advanced technology curve, there's a lot of interest around the benefits of FinFET technology over existing planar CMOS transistors. In this video, Dr. Rob Aitken, R ...Investing in FinFET Technology Leadership Presented by ARM2 m) Technology Node 0.092 um2 ~0.5x per generation SRAM Cells 0.092 um2 2and 0.108 um SRAM cells optimized for density and performance/power . 26 SRAM Array V MIN 0.60V medium active V MIN for low power applications ... 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But he established a stable dual CEO management system and competitive new technology to keep things moving after departure.How Taiwan Chipmaker TSMC Will Prosper For 5 More ... - ForbesThe new performance-enhanced processes include the 7nm-based N7P (an upgrade over the current N7 process) and 5nm-based N5P (N5's successor). TSMC made it clear that the development of its ...TSMC introduces N7P (7nm), N5P (5nm) processes, 3nm ...Published on Jun 2, 2016 I am going to discuss the next generation of devices that will revitalize Moore's law and enable sub-5 nm technology. Specifically, I would like to talk about two ...2D Field Effect Transistor (FET) for Sub-5 nm Technology NodeTaiwan Semiconductor Manufacturing Co. Ltd. (NYSE:TSM) Q2 2019 Earnings Conference Call July 18, 2019 02:00 AM ET Company Participants Elizabeth Sun - Senior DiTaiwan Semiconductor Manufacturing Co. Ltd. (TSM) CEO Dr ...TSMC Technology Roadmaps TSMC had their annual Technology Symposium last week. As always, my post comes with a caveat: TSMC will not allow pictures to be taken, recordings to be made, and they don't hand out any materials.TSMC Technology Roadmaps - Cadence CommunityDr. C.C. Wei, TSMC President, and Co-CEO conveyed that artificial intelligence (AI) and 5G are the key drivers of our industry's growth and reminded the audience that TSMC is committed to being a trusted technology and capacity partner. To demonstrate this commitment, Wei mentioned that TSMC employs more than 6000 research and development (R&D) experts and was first with 28, 16 and 10 nm as ...

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**2D Field Effect Transistor (FET) for Sub-5 nm Technology Node**

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nm Gate Pitch 54 66 68 nm Minimum Metal Pitch 36 42 48 nm Logic Cell Height 272 360 420 nm  
Logic Trans. Density 100.8 48.1 51.6 MTr/mm<sup>2</sup> ... Mark Bohr on Intel's Technology Leadership ...  
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leadership development of 5nm with the world's smallest SRAM cell at 0.021um<sup>2</sup> with circuit design

details of their write assist techniques necessary to achieve the full potential of this revolutionary  
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